



SV COLLEGE OF ENGINEERING (AUTONOMOUS)

Karakambadi Road, Tirupati-517507

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EXAMINATION BRANCH

M.Tech I Semester II Internal Examinations March-2023 TIME TABLE

FN: 10.00 am to 12.00 pm

Date/Day	VLSI Design (VLSI D)	Computer Science and Engineering (CSE)
20-03-2023 (Monday)	CMOS Analog IC Design	Mathematical Foundations of Computer Science
21-03-2023 (Tuesday)	CMOS Digital IC Design	Advanced Data Structures
23-03-2023 (Thursday)	Nanomaterials & Nanotechnology	Machine Learning
24-03-2023 (Friday)	CPLD and FPGA Architectures and Applications	Data Science
25-03-2023 (Saturday)	Research Methodology and IPR	Research Methodology and IPR

P. Raju

N. Reddy

CONTROLLER OF EXAMINATIONS

PRINCIPAL

Copy To:

1. ACE I/II/III.
2. HOD (ECE & CSE) with a request to circulate among Staff and students and make arrangements to send all the question papers (soft copy & hard copy) to the Exam cell on or before **13-03-2023, 04.00 P.M.**
3. All Notice Boards, T & P Cell, I/C Security, I/C Hostel, I/C Transport, I/C Canteen and AO for information.

Date: 01/03/2023